"Observing partial orders in concurrent systems."

5:30-7:30 **Workshop Dinner**
(served in Gallery outside talks Computer Science Bldg.)

Friday, July 26th

Session 9 **Branching Time**
Chair: D. Peled

9:00 - 9:50 **A. Emerson**
"Branching Time Temporal Logics Methods for the Verification of Concurrent Systems"

9:50 - 10:20 **P. Wolper**
"Partial-Order Methods for Model Checking: From Linear Time to Branching Time"

10:20 - 10:40 *Coffee Break*

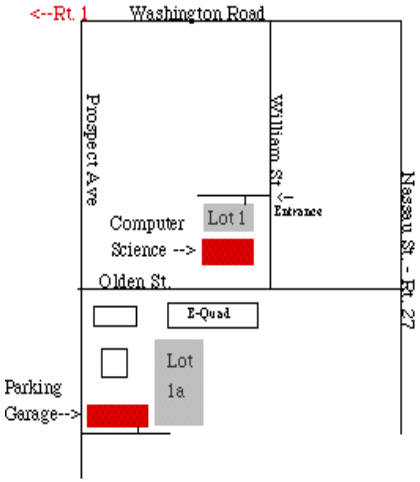
Session 10 **Practical Methods**
Chair: R. van Glabbeek

10:40 - 11:10 **P. Godefroid**
"On the Costs and Benefits of using Partial-Order Methods for the Verification of Concurrent Systems"

11:10 - 11:40 **E. Best**
"Partial Order Verification with PEP"

11:40 - 12:10 **D. Luckham**
"Rapide: A Language and Toolset for Simulation of Distributed Systems by Partial Orderings of Events"

12:00 *Conclusion of conference.*



Park in the parking garage located on Olden Street on the 4th or 5th floor.

sponsored by

ONR

DIMACS
Discrete Mathematics & Theoretical Computer Science

AT&T

Lucent Technologies

POMIV 96:
Partial Order Methods in Verification

July 24, 25, & 26, 1996

DIMACS Workshop

Princeton University
Dept. of Computer Science
35 Olden Street
Princeton, NJ 08544-2087

Program Organizers

Doron Peled
Bell Labortories
doron@research.bell-labs.com

Gerard Holzmann
Bell Labortories
gerard@research.bell-labs.com

Vaughan Pratt
Stanford University
pratt@cs.stanford.edu

Princeton University
Dept. of Computer Science
35 Olden Street
Room 105, Small Auditorium
Princeton, NJ

Tuesday, July 23

7:00-9:00 pm **Reception**
Prospect Gardens
Main Campus
Princeton University

Wednesday, July 24

8:00 - 8:45 **Welcome & Information**

Session 1: Trace Theory
Chair: V. Pratt

9:00 - 9:50 **A. Mazurkiewicz**
"A prefix function view of states and events"

9:50 - 10:20 **V. Diekert**
"Infinite traces"

10:20 - 10:40 *Coffee Break*

Session 2: States & Locations
Chair: A. Emerson

10:40 - 11:30 **W. Thomas**
"Automata Theory over partial orders"

11:30 - 11:40 *Break*

11:40 - 12:10 **I. Castellani**
"A survey of location based semantics"

12:40 - 2:00 *Lunch*

Session 3 Partial Order Verification
Chair: P.S. Thiagarajan

2:00 - 2:30 **S. Katz**
"Proving Execution Sequences Equivalent Under a Partial Order"

2:30 - 3:00 **W. Penczek**
"A complete axiomatization of a first-order temporal logic over trace systems"

3:00 - 3:30 **W. Reisig**
"The notion of rounds in distributed algorithms and their correctness proof"

3:30 - 4:00 *Coffee Break*

Session 4 Semantics
Chair: M. Nielsen

4:00 - 4:30 **V. Pratt**
"From partial orders to partial distributive lattices"

4:30 - 5:20 **G. Winskel**
"Presheaf models for concurrency"

Thursday, July 25

Session 5 Models of Concurrency
Chair: G. Winskel

9:00 - 9:50 **M. Nielsen**
"Partial Order Games and Logics"

9:50 - 10:20 **M.Z. Kwiatkowska**
"On topological hierarchies of temporal properties"

10:20 - 10:40 *Coffee Break*

Session 6 Concurrency Theory
Chair: W. Thomas

10:40 - 11:30 **P.S. Thiagarajan**
"Branching Time Partial Order Based Behaviors"

11:30 - 11:40 *Coffee Break*

11:40 - 12:10 **A. Rabinovich**
"A solution of an interleaving decision problem by a partial order technique"

12:10 - 12:40 **R. Kuiper**
"Partial-order logics: complexity and expressiveness"

12:40 - 2:00 *Lunch*

Sessions 7 Partial Order Verification
Chair: G. Holzmann

2:00 - 2:30 **A. Valmari**
"Stubborn Set Methods for Process Algebras"

2:30 - 3:00 **D. Peled**
"Partial Order Reduction: Linear and Branching Temporal Logics and Process Algebras"

3:00 - 3:30 **M. Pistore**
"History Dependent Verification for Partial Order Systems"

3:30 - 4:00 *Coffee Break*

Sessions 8 Models of Concurrency
Chair: A. Mazurkiewicz

4:00 - 4:30 V.Sassone
"Transition Systems with Independence vs. Asynchronous Transition Systems"

4:30 - 5:20 **R. van Glabbeek**

ERROR: stackunderflow
OFFENDING COMMAND: exch
STACK:
/picBottom